

Figure S1

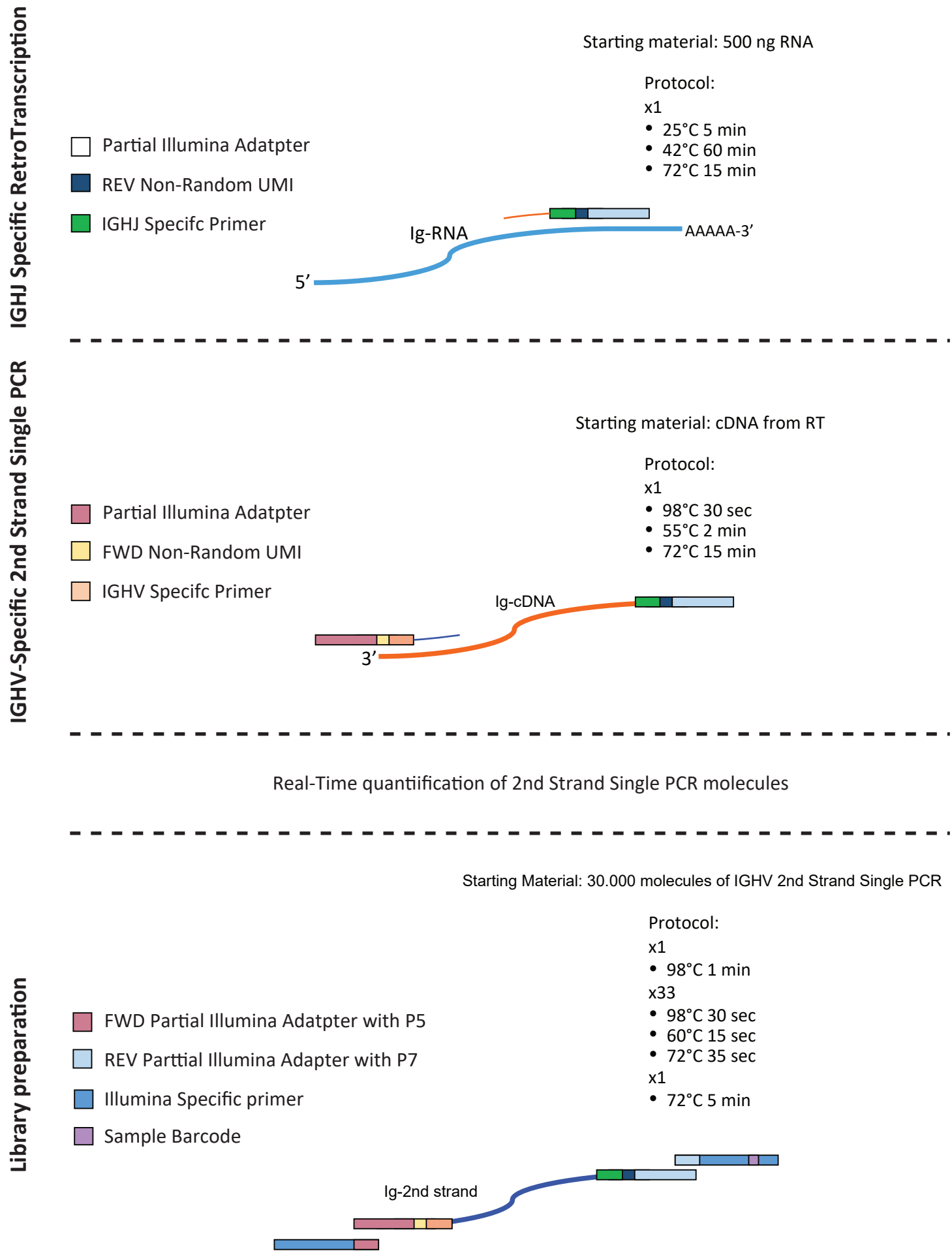
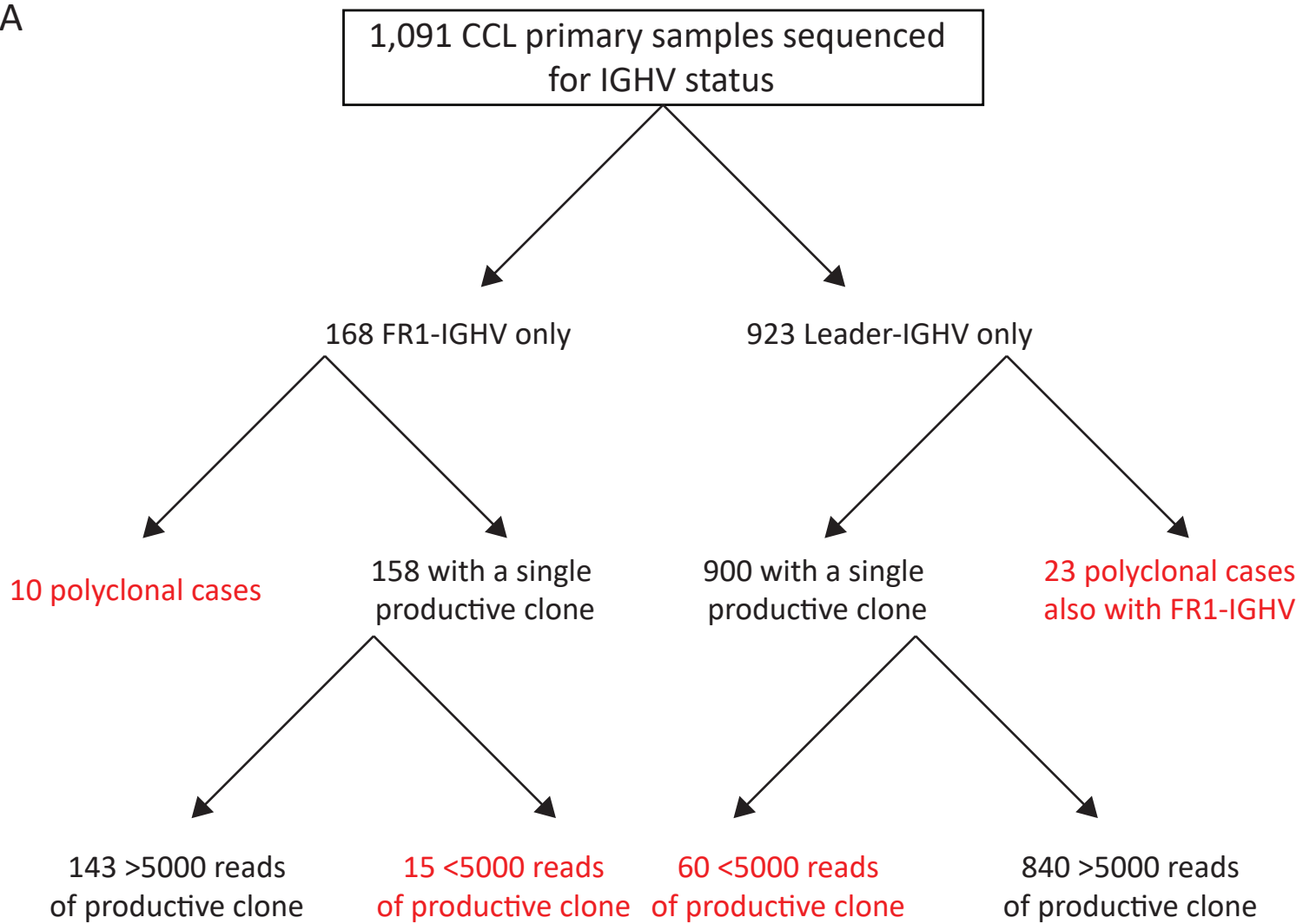


Figure S2

A



B

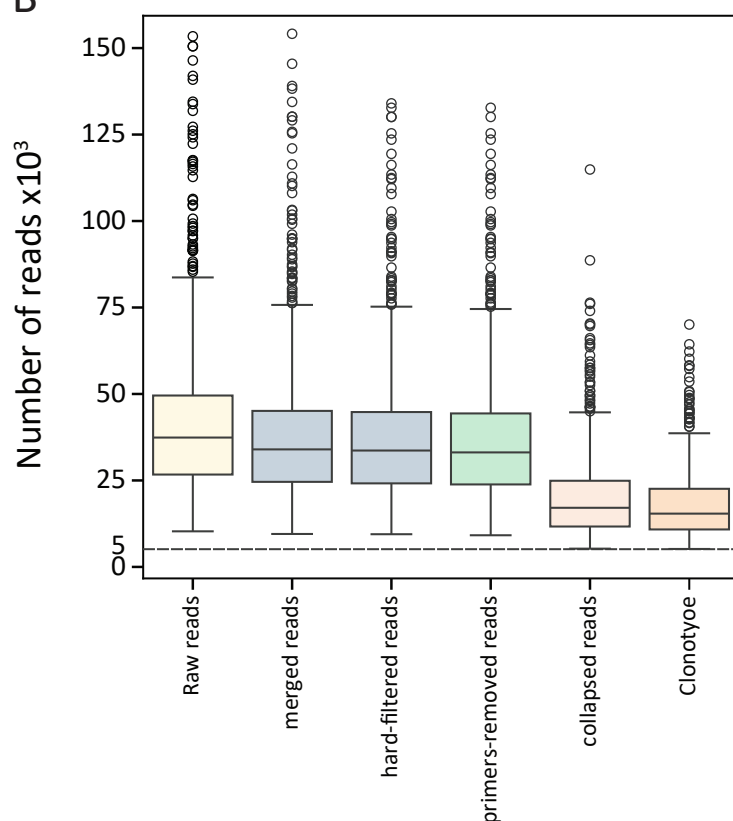


Figure S3

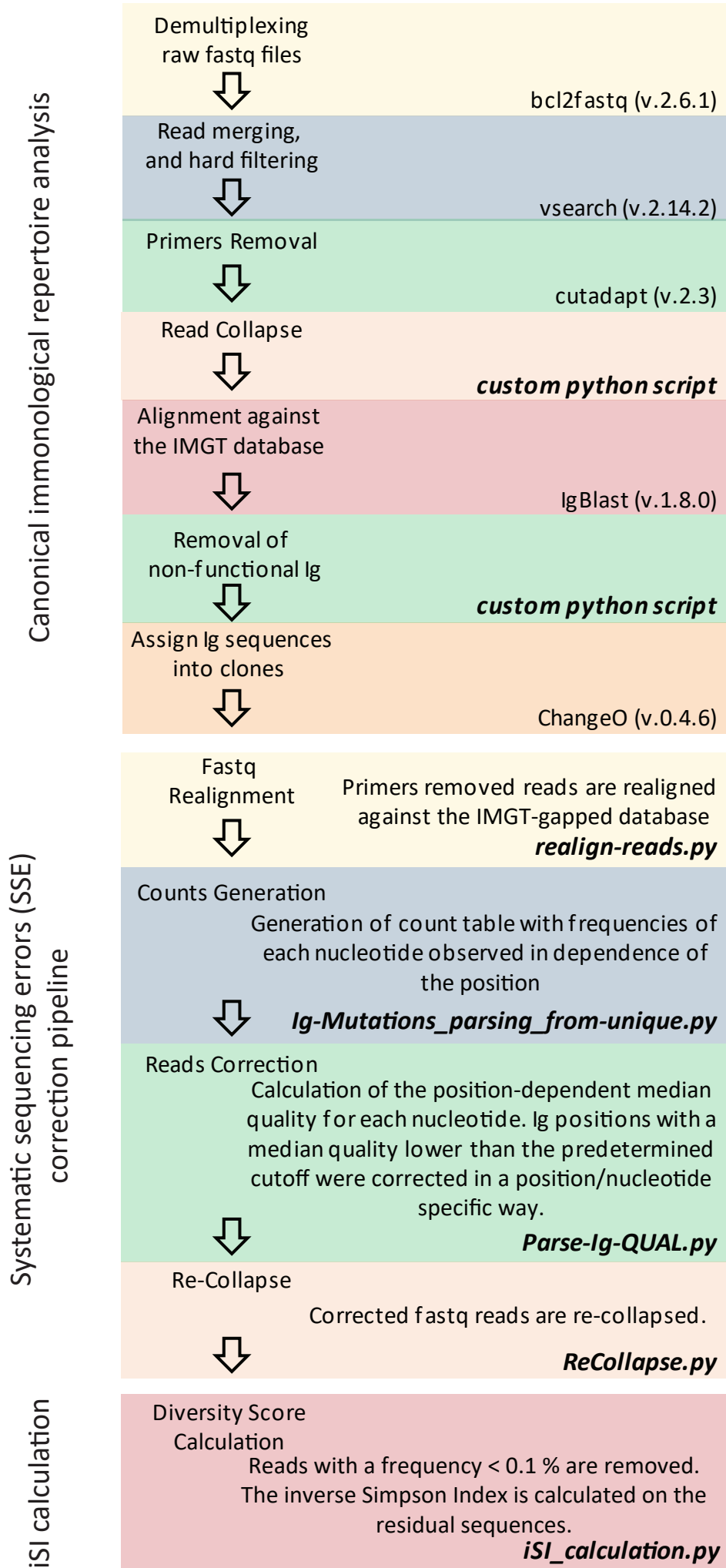


Figure S4

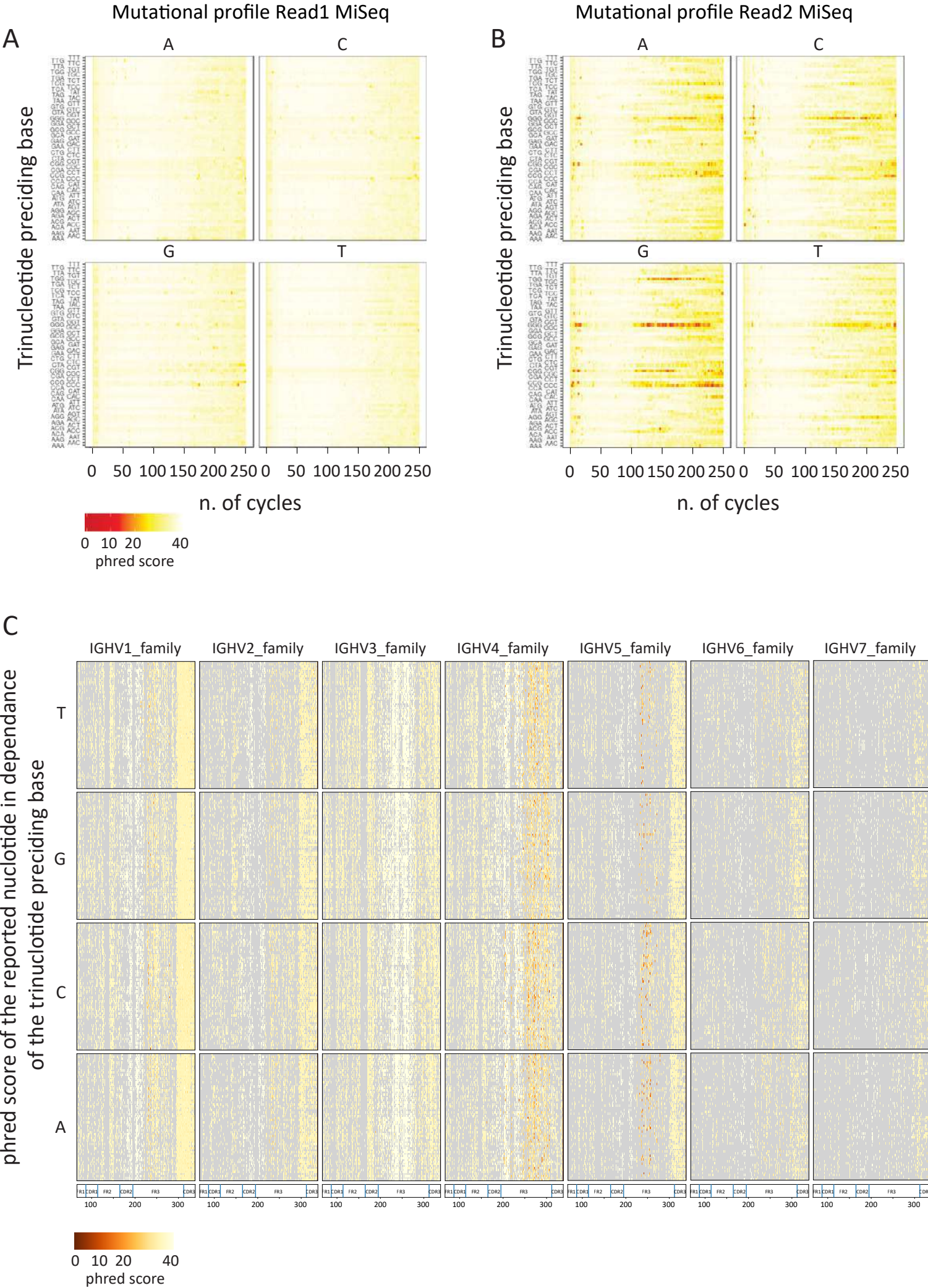


Figure S5

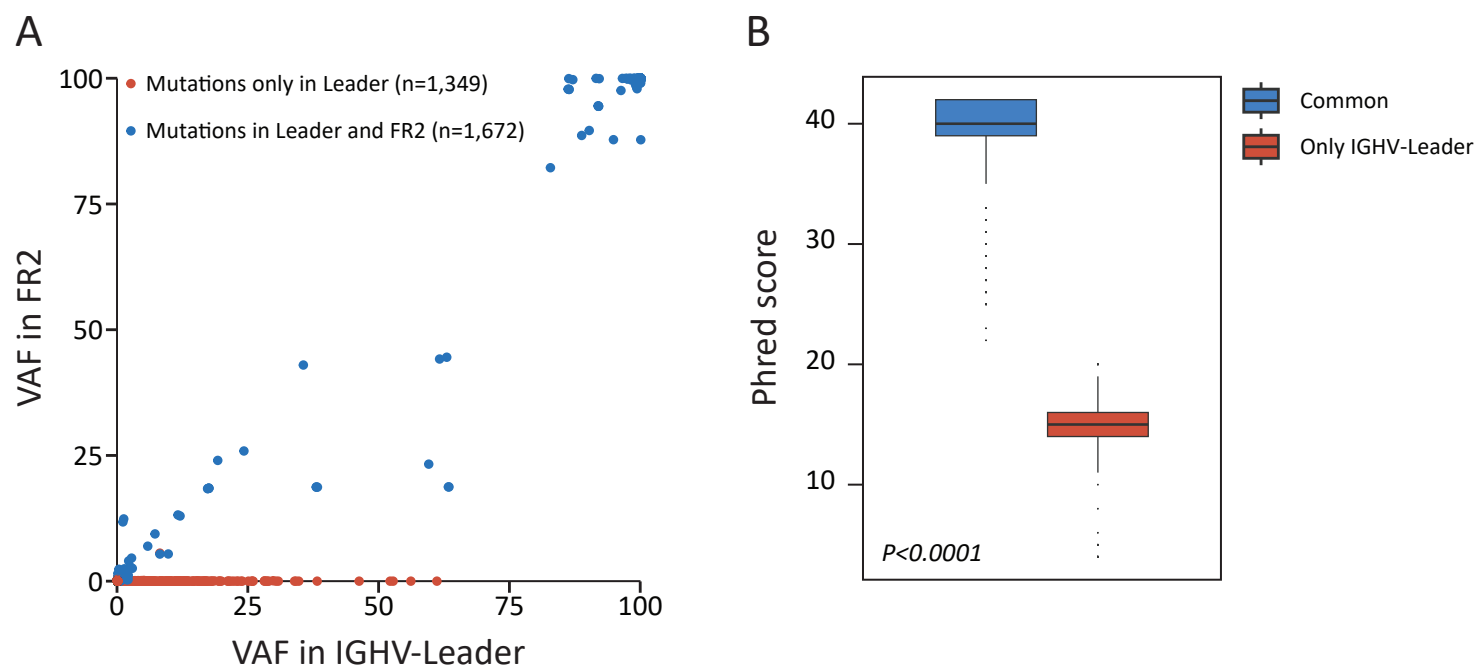


Figure S6

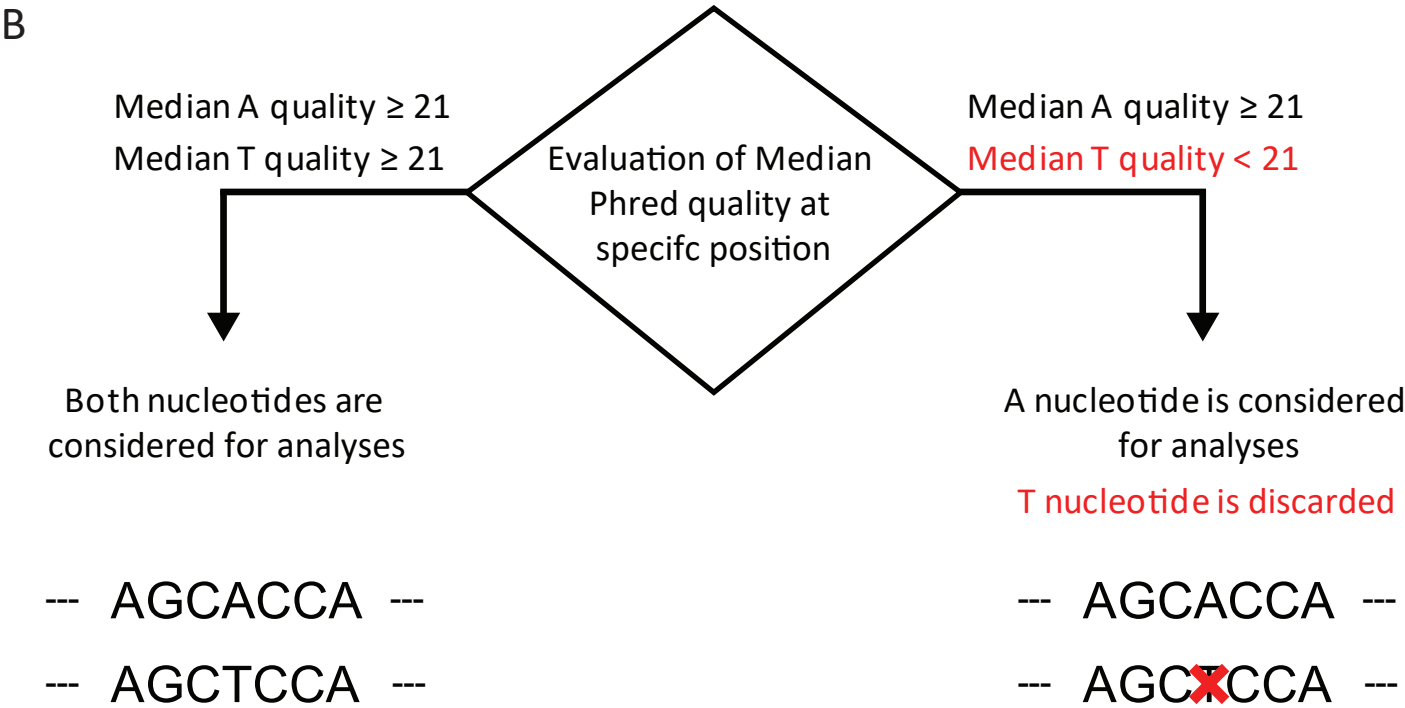
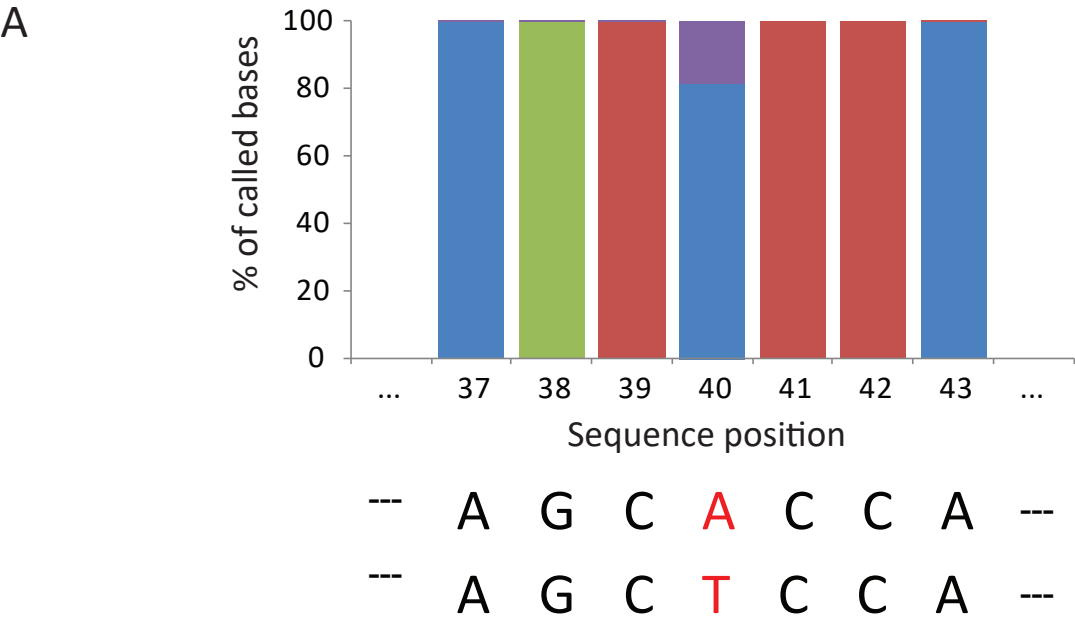


Figure S7

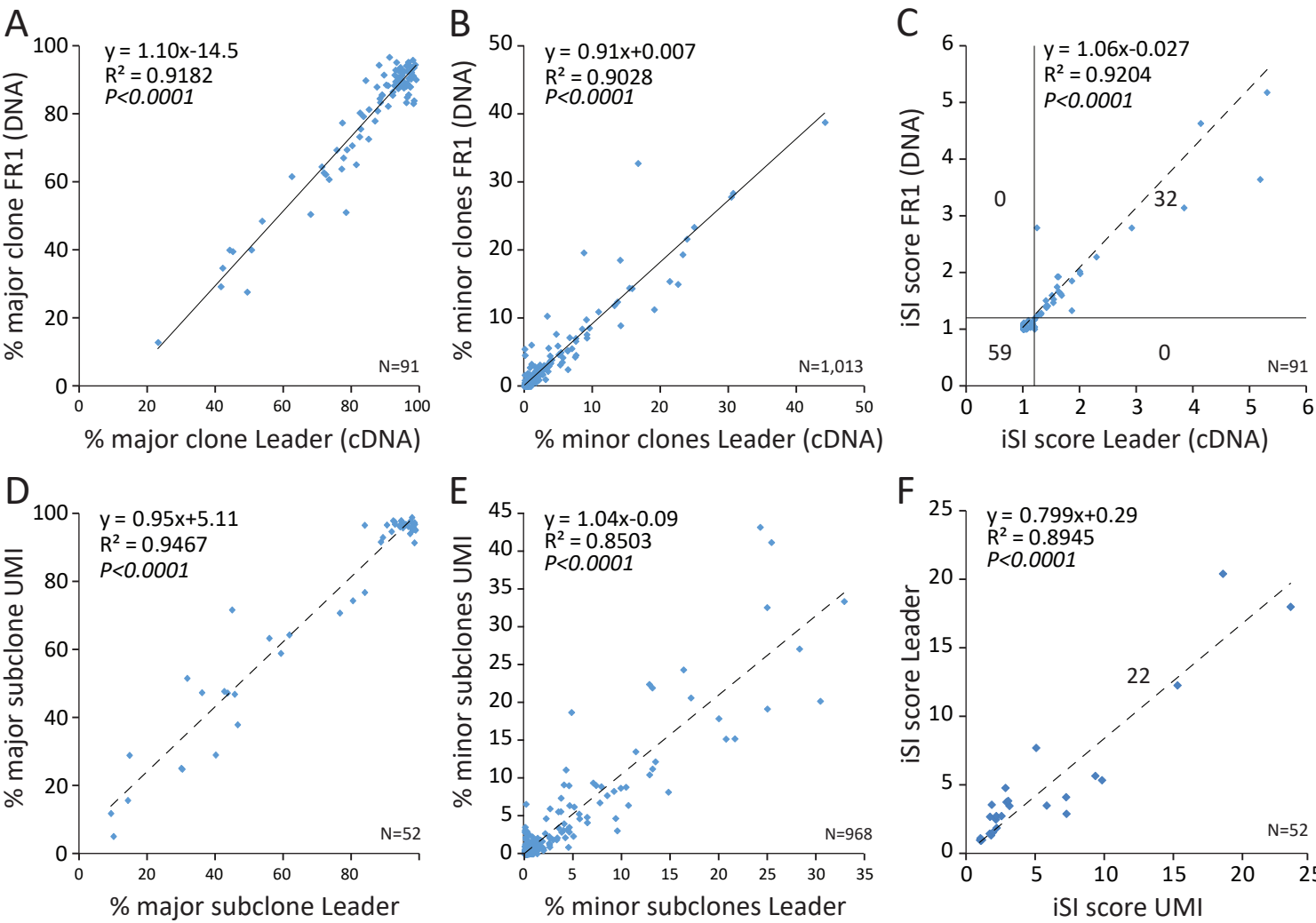
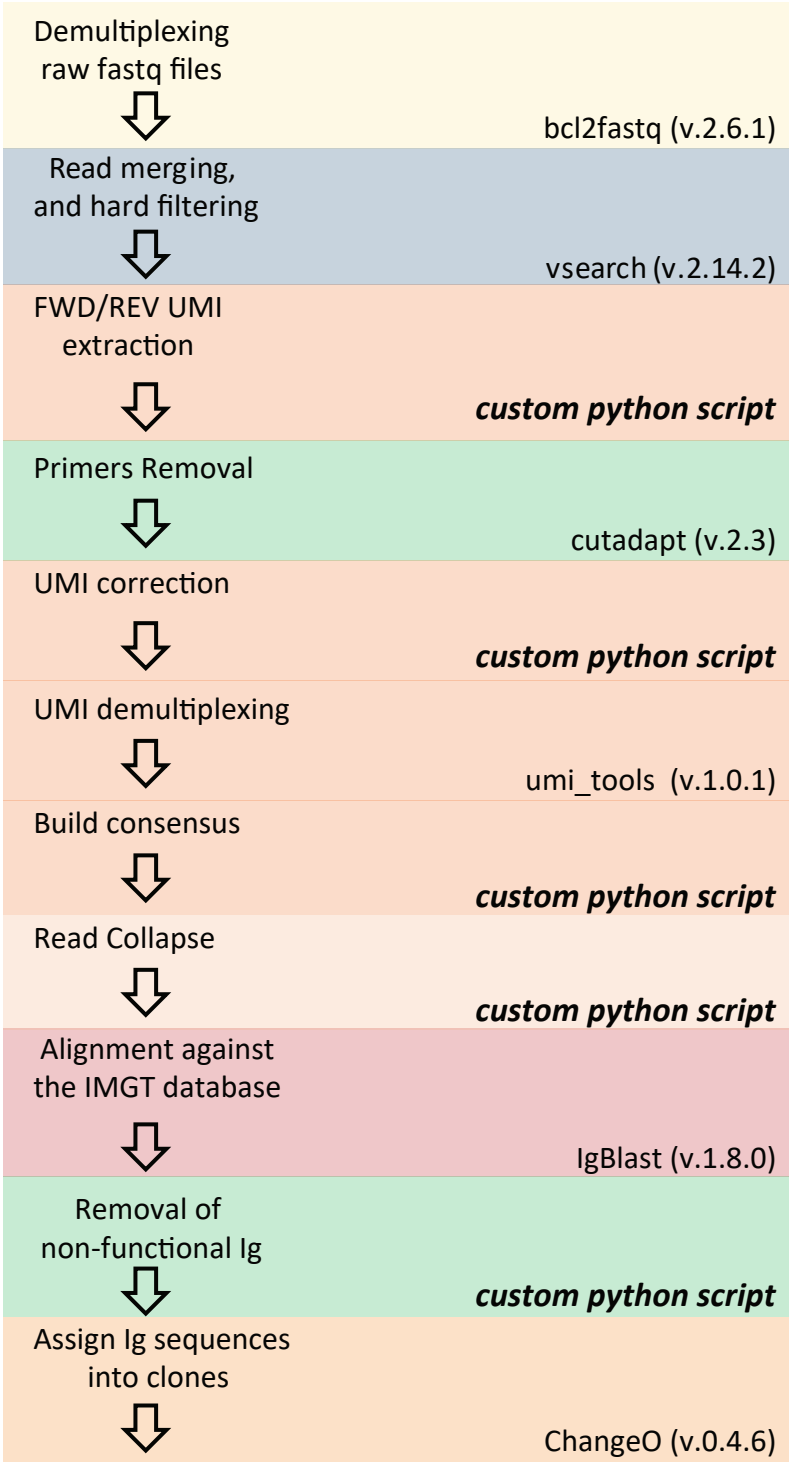


Figure S8

Steps for UMI Analysis and Error-correction



see SSE correction pipeline, Figure S2

iSI calculation

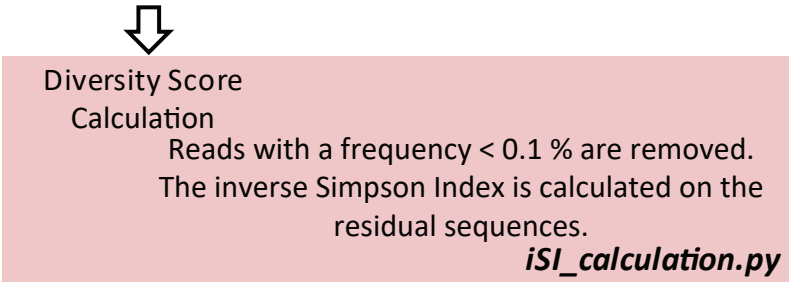


Figure S9

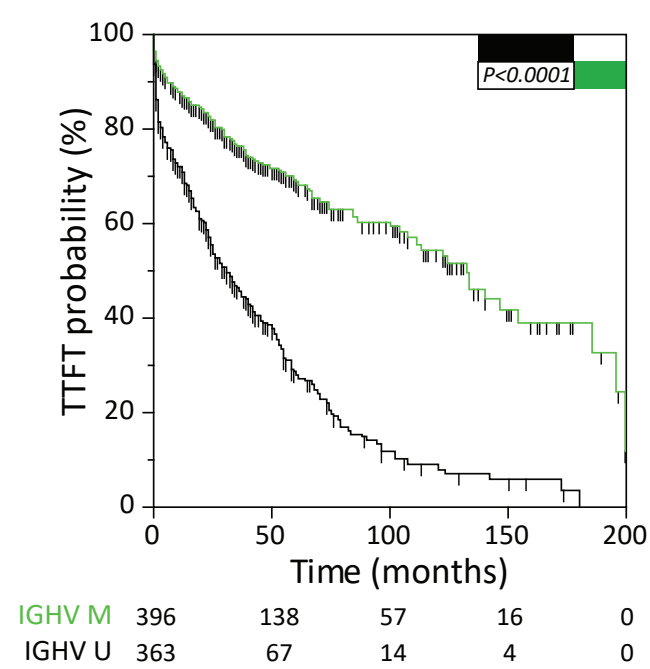


Figure S10

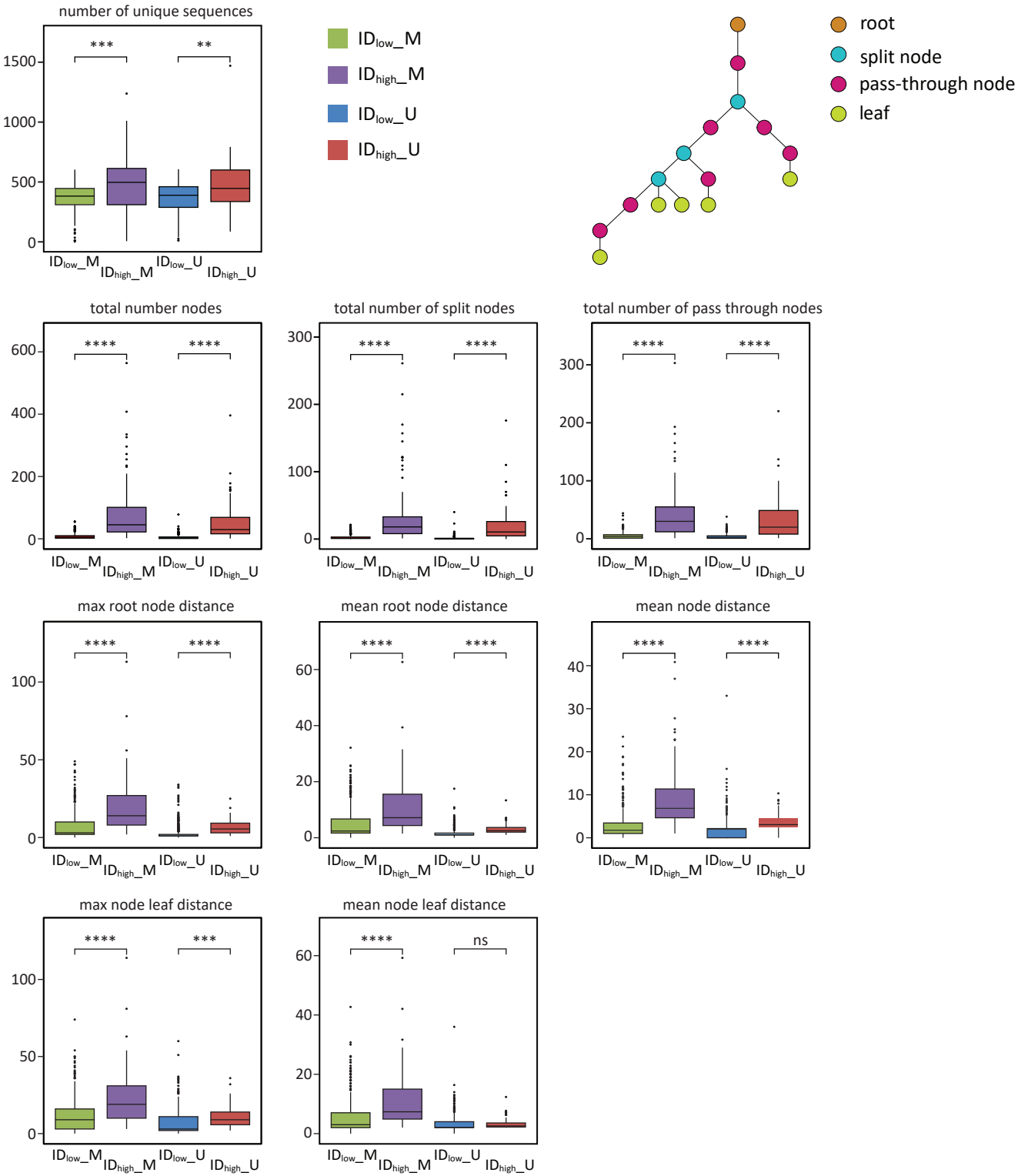


Figure S11

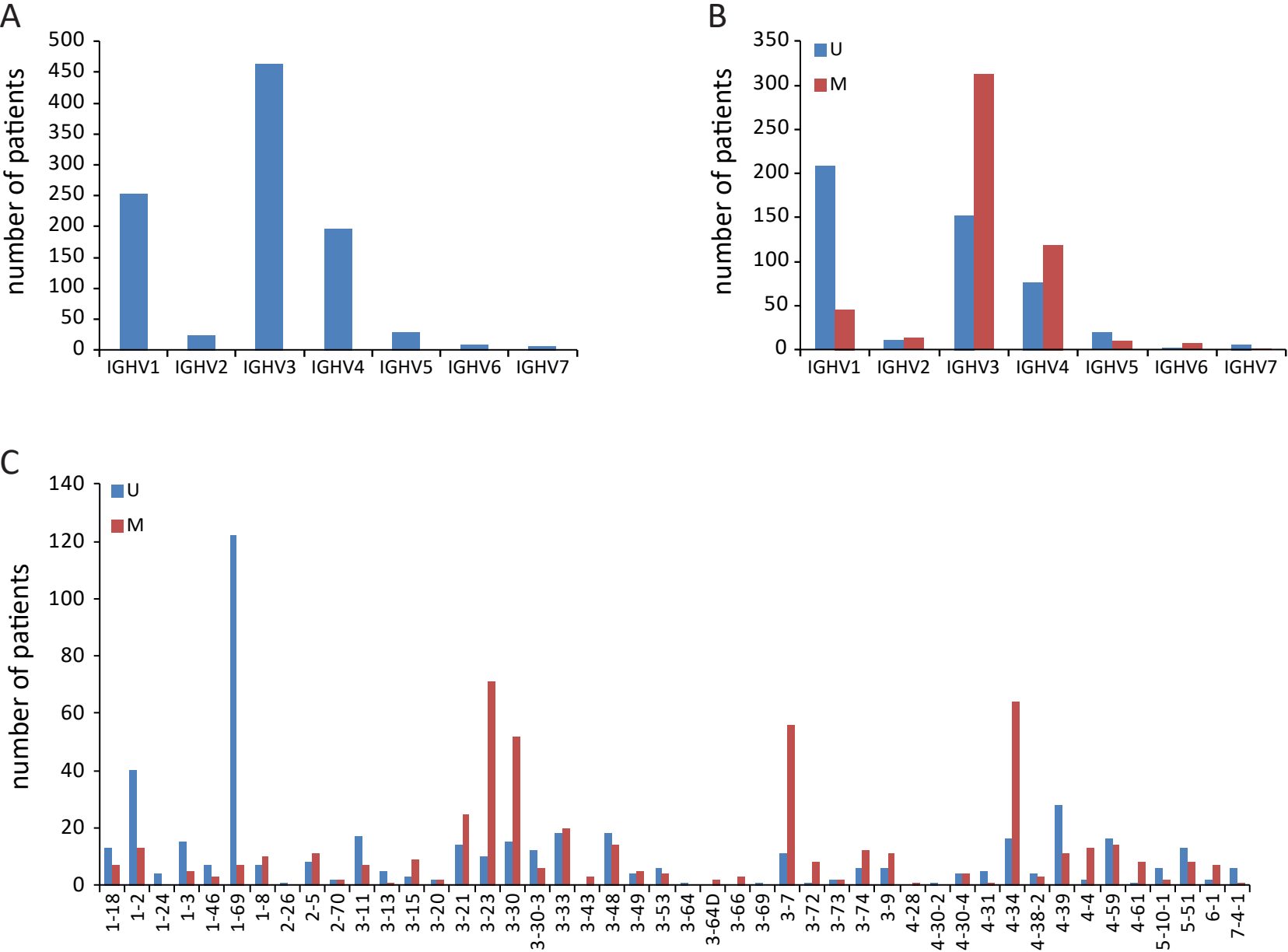


Figure S12

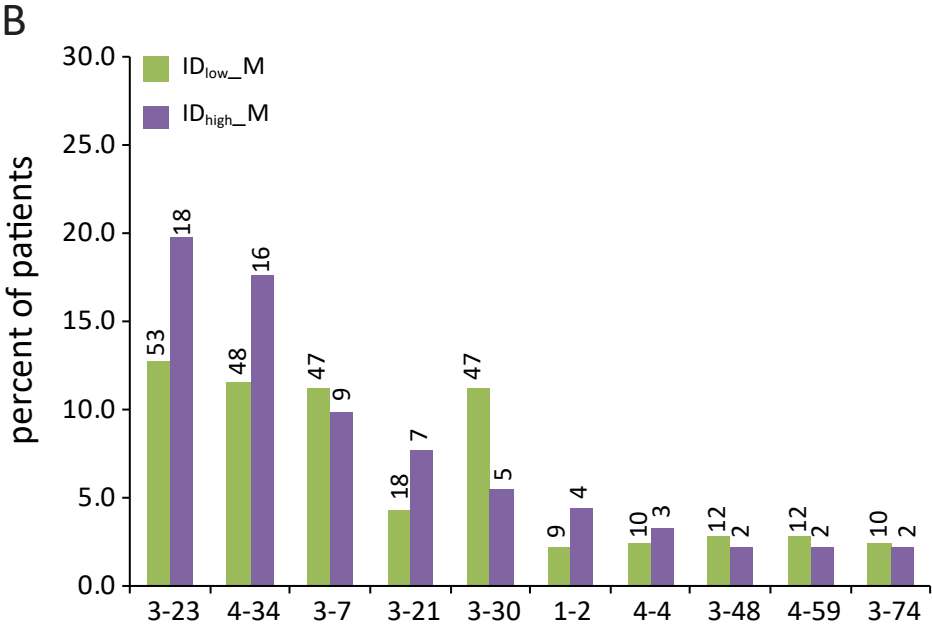
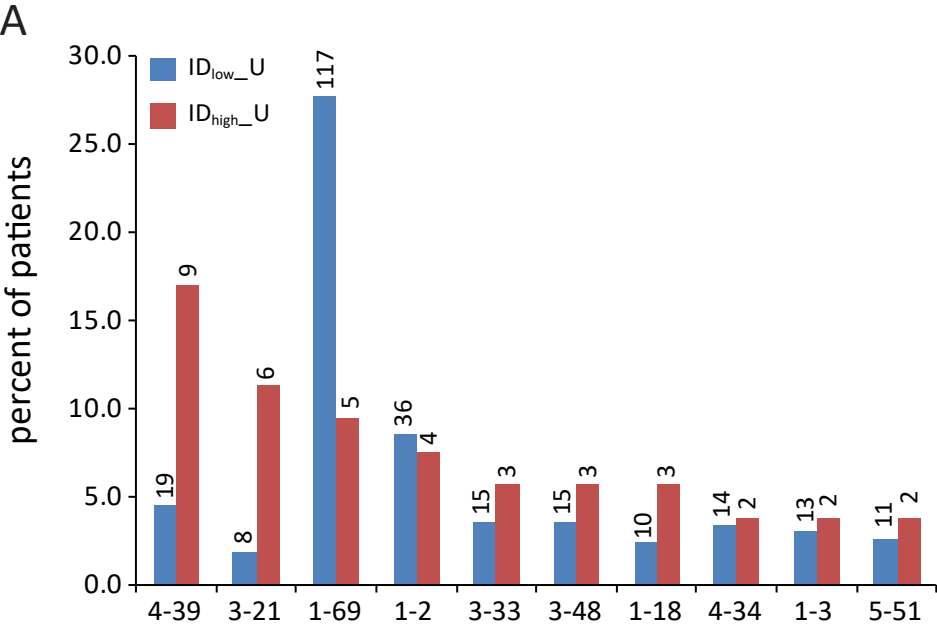


Figure S13

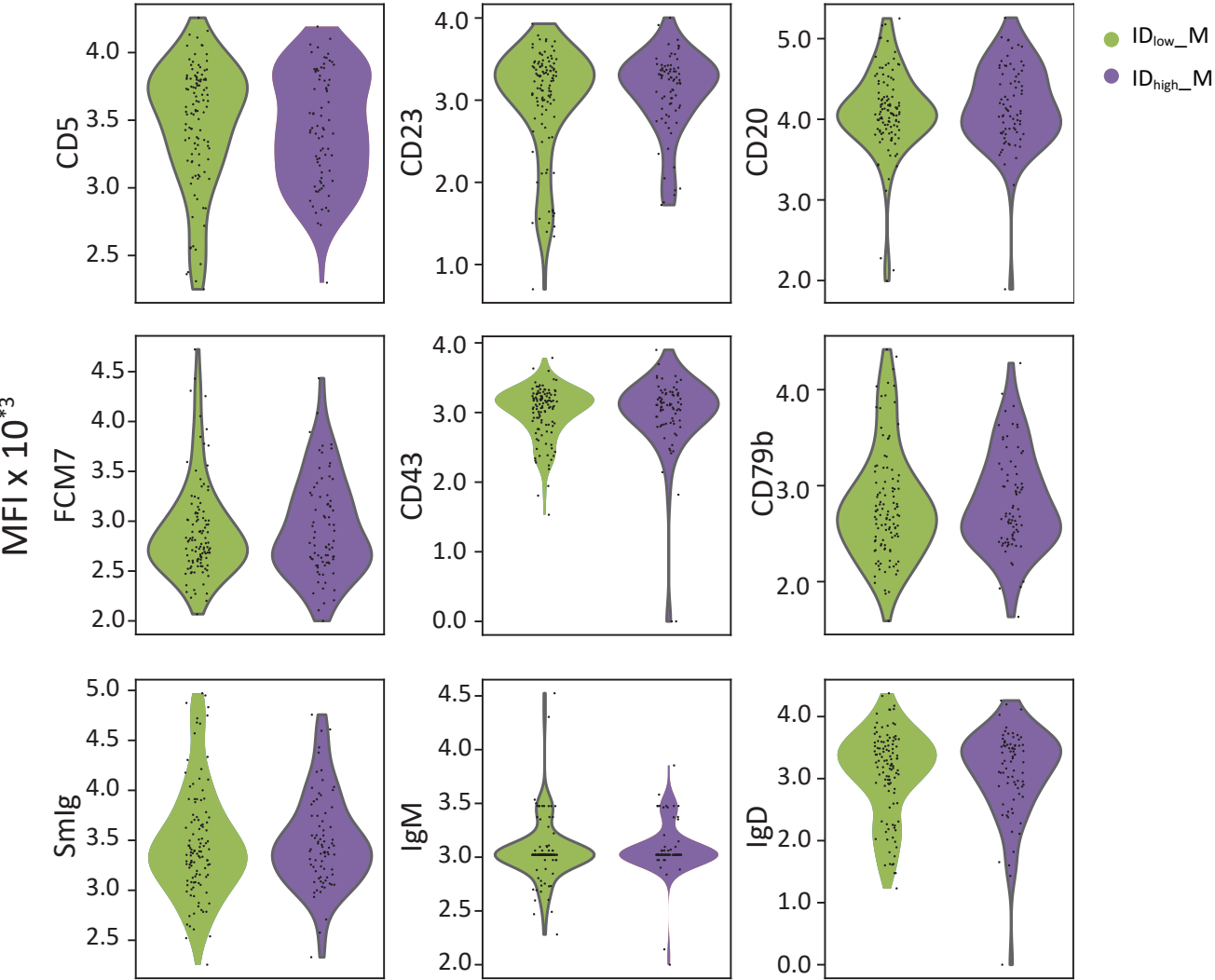


Figure S14

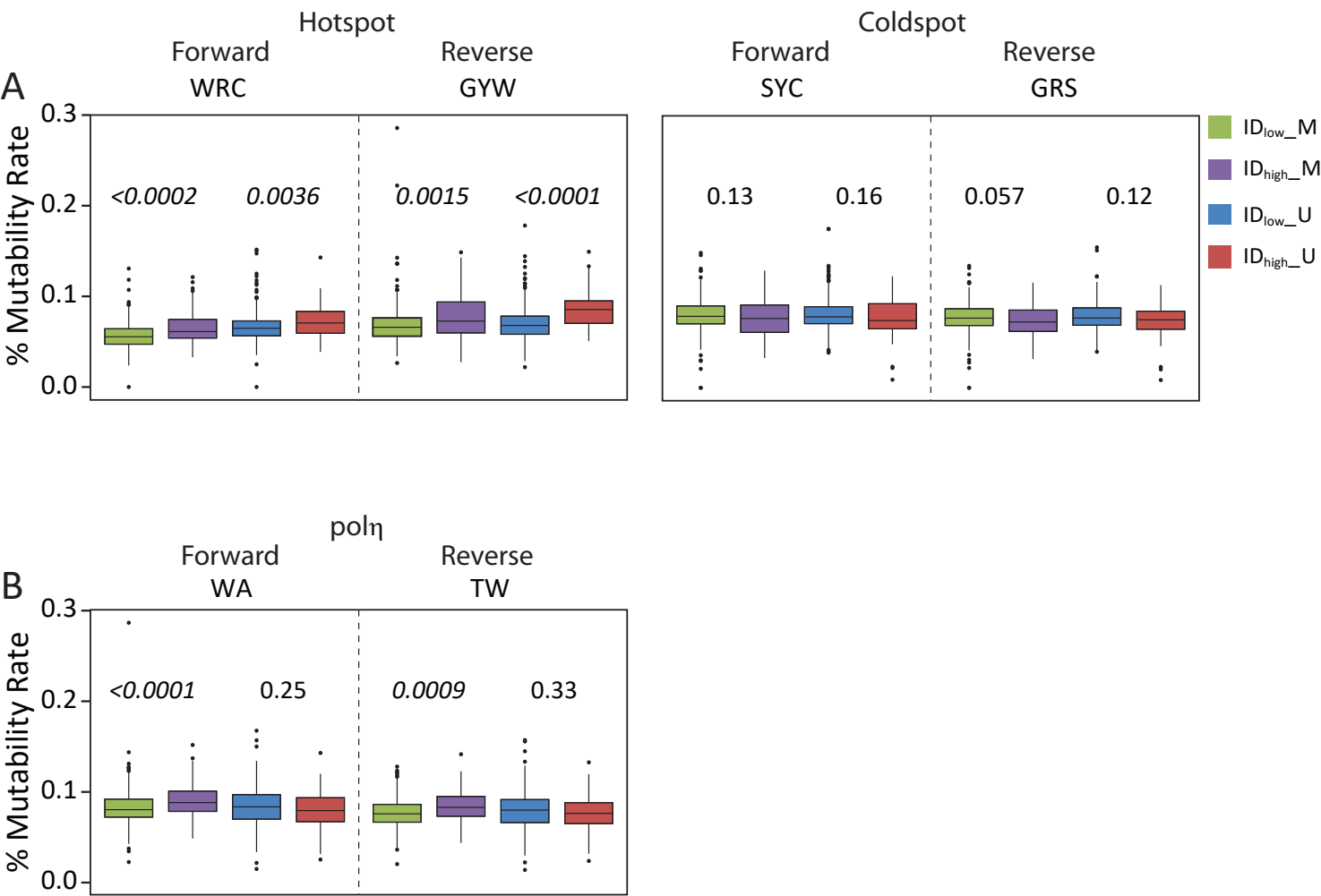


Figure S15

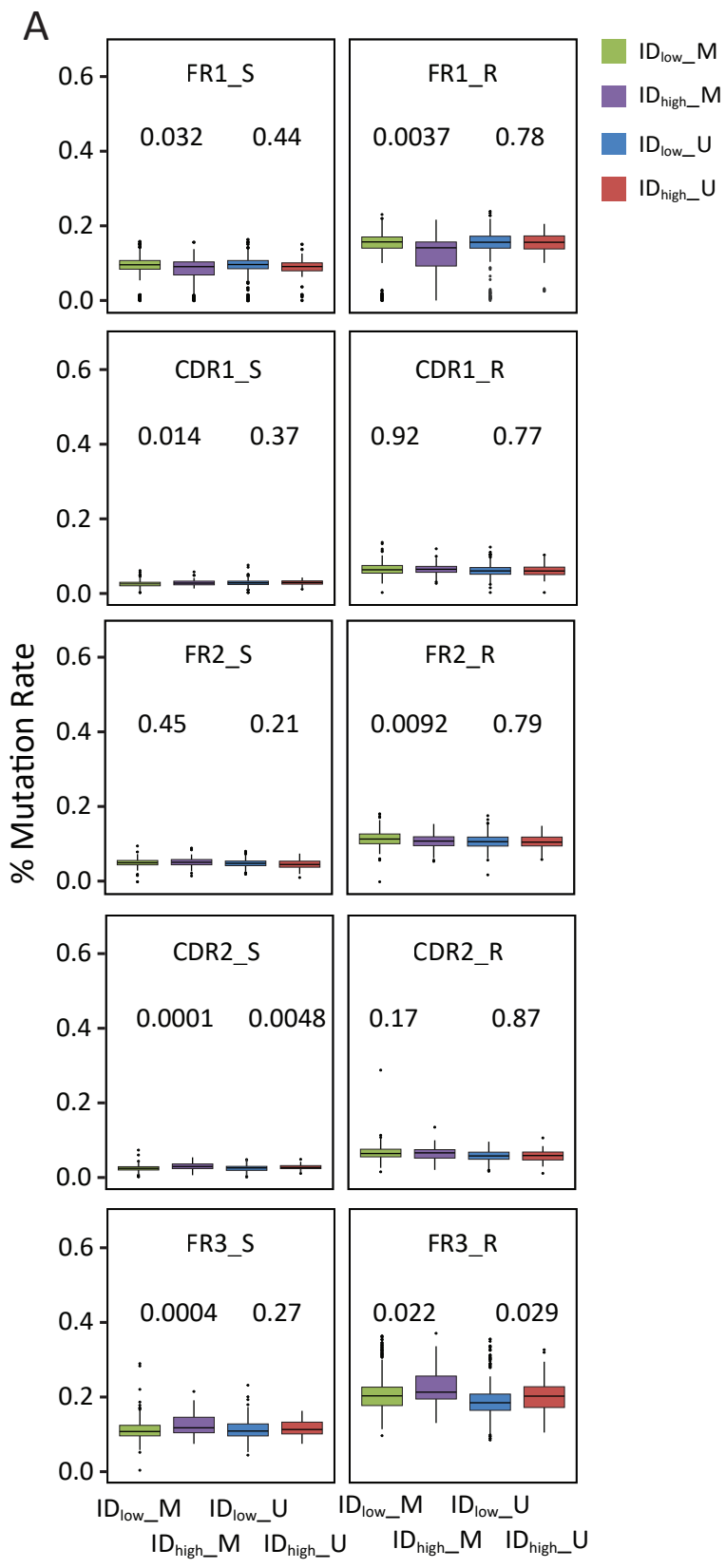


Figure S16

